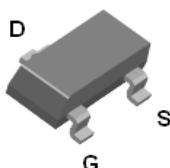


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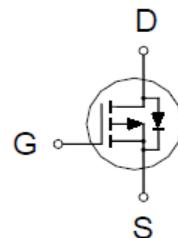
P-Channel Enhancement Mode MOSFET

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
-20V	40mΩ @ $V_{GS} = -4.5V$	-4A



SOT-23(S)



ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ C$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS	SYMBOL	LIMITS	UNITS
Gate-Source Voltage	V_{GS}	± 8	V
Continuous Drain Current	I_D	-4	A
		-3	
Pulsed Drain Current ¹	I_{DM}	-21	
Power Dissipation	P_D	0.9	W
		0.6	
Operating Junction & Storage Temperature Range	T_j, T_{stg}	-55 to 150	°C

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Ambient ²	$R_{\theta JA}$		126	°C / W

¹Pulse width limited by maximum junction temperature.

²The value of $R_{\theta JA}$ is measured with the device mounted on 1in² FR-4 board with 2oz.Copper.



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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

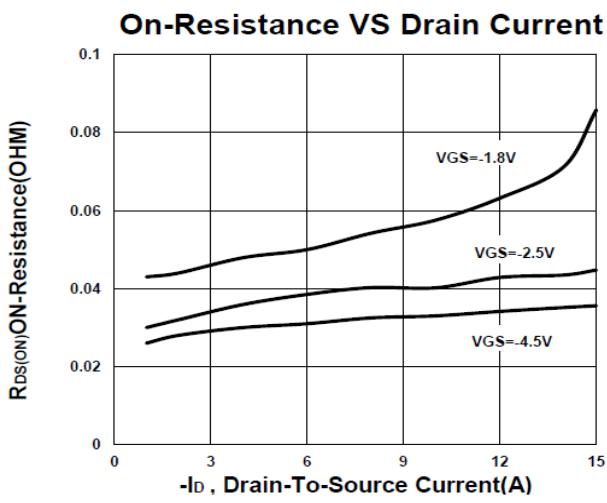
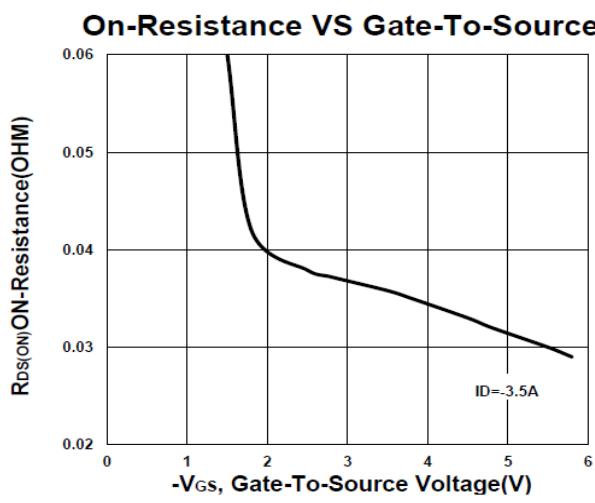
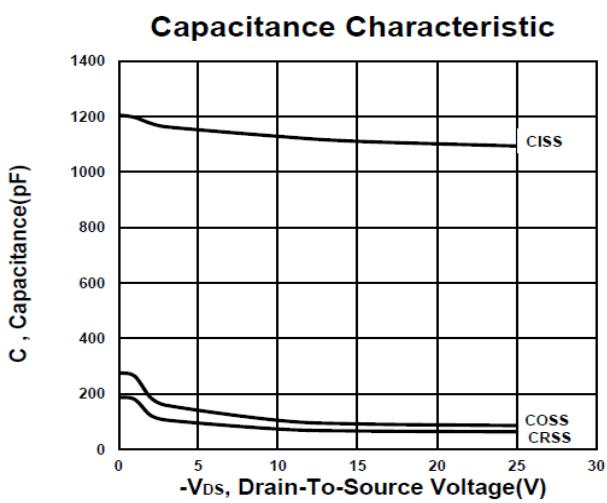
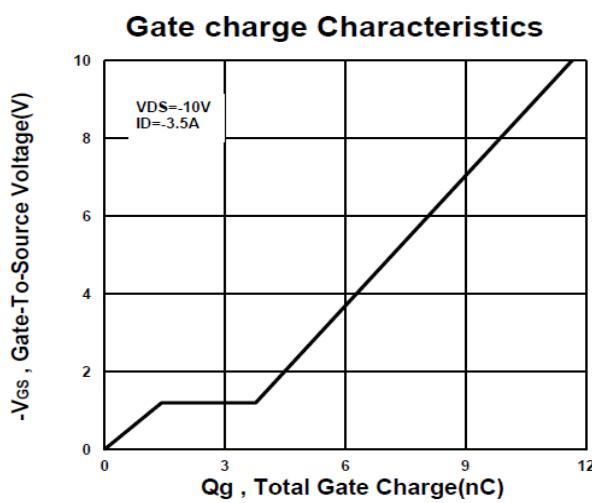
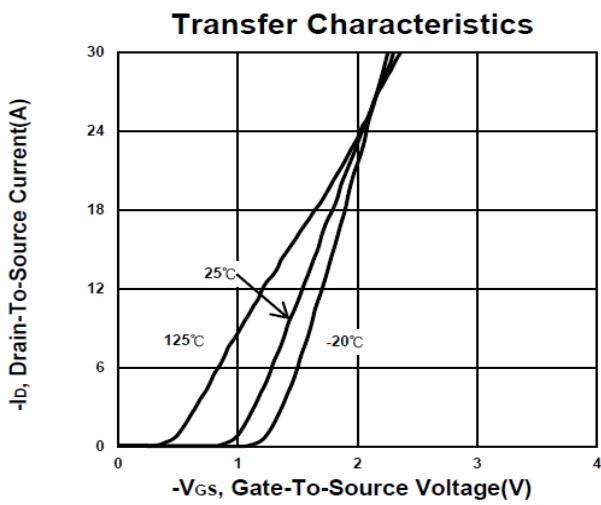
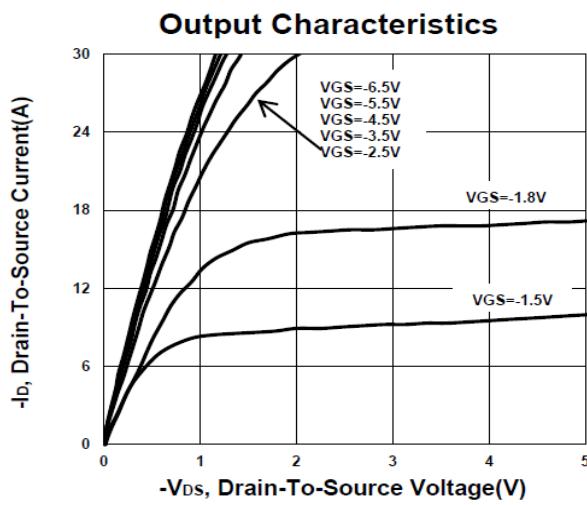
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNITS
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = -250\mu\text{A}$	-20			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = -250\mu\text{A}$	-0.3	-0.6	-1	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0\text{V}, V_{\text{GS}} = \pm 8\text{V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = -16\text{V}, V_{\text{GS}} = 0\text{V}$			-1	μA
		$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 55^\circ\text{C}$			-10	
Drain-Source On-State Resistance ¹	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}} = -1.5\text{V}, I_D = -1\text{A}$		67	100	$\text{m}\Omega$
		$V_{\text{GS}} = -1.8\text{V}, I_D = -2\text{A}$		50	71	
		$V_{\text{GS}} = -2.5\text{V}, I_D = -3.5\text{A}$		40	55	
		$V_{\text{GS}} = -4.5\text{V}, I_D = -3.5\text{A}$		33	40	
Forward Transconductance ¹	g_{fs}	$V_{\text{DS}} = -5\text{V}, I_D = -3.5\text{A}$		17		S
DYNAMIC						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = -10\text{V}, f = 1\text{MHz}$		1157		pF
Output Capacitance	C_{oss}			106		
Reverse Transfer Capacitance	C_{rss}			76		
Total Gate Charge ²	Q_g	$V_{\text{DS}} = -10\text{V}, V_{\text{GS}} = -4.5\text{V}, I_D = -3.5\text{A}$		11.9		nC
Gate-Source Charge ²	Q_{gs}			1.6		
Gate-Drain Charge ²	Q_{gd}			2.5		
Turn-On Delay Time ²	$t_{\text{d(on)}}$	$V_{\text{DD}} = -10\text{V}, V_{\text{GS}} = -4.5\text{V}$ $I_D \approx -3.5\text{A}, R_G = 6\Omega$		22		nS
Rise Time ²	t_r			20		
Turn-Off Delay Time ²	$t_{\text{d(off)}}$			59		
Fall Time ²	t_f			13		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS ($T_J = 25^\circ\text{C}$)						
Continuous Current	I_S				-0.6	A
Forward Voltage ¹	V_{SD}	$I_F = -3.5\text{A}, V_{\text{GS}} = 0\text{V}$			-1.3	V
Reverse Recovery Time	t_{rr}	$I_F = -3.5\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$		11.4		nS
Reverse Recovery Charge	Q_{rr}			2.9		nC

¹Pulse test : Pulse Width $\leq 300\ \mu\text{sec}$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

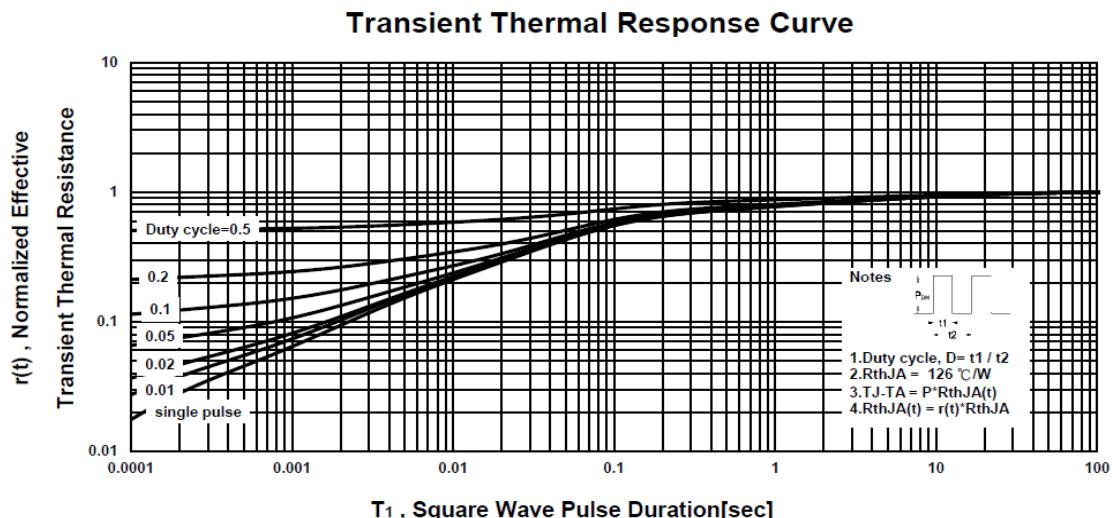
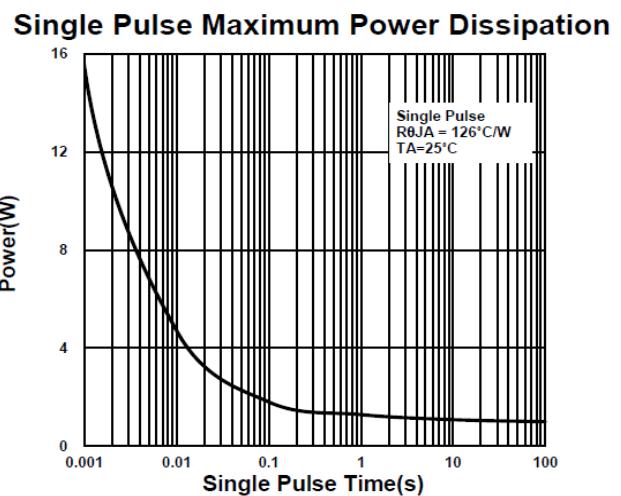
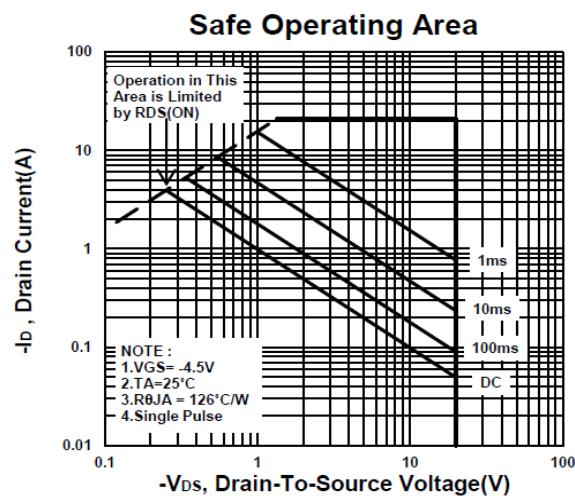
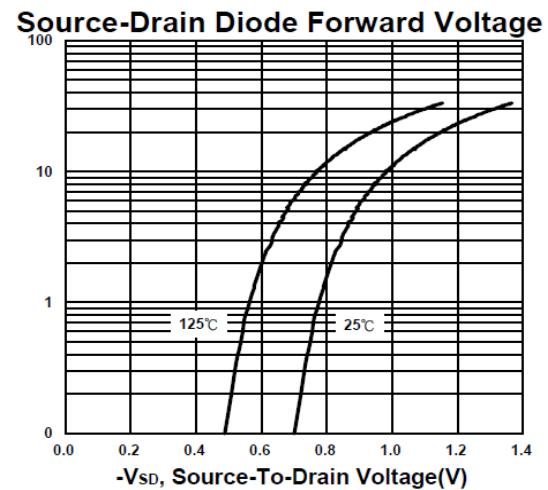
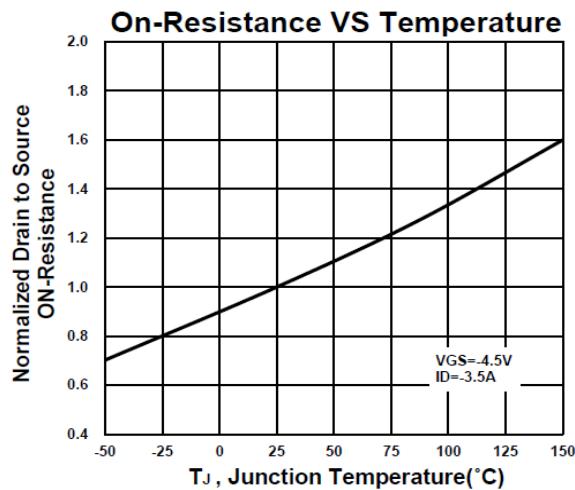
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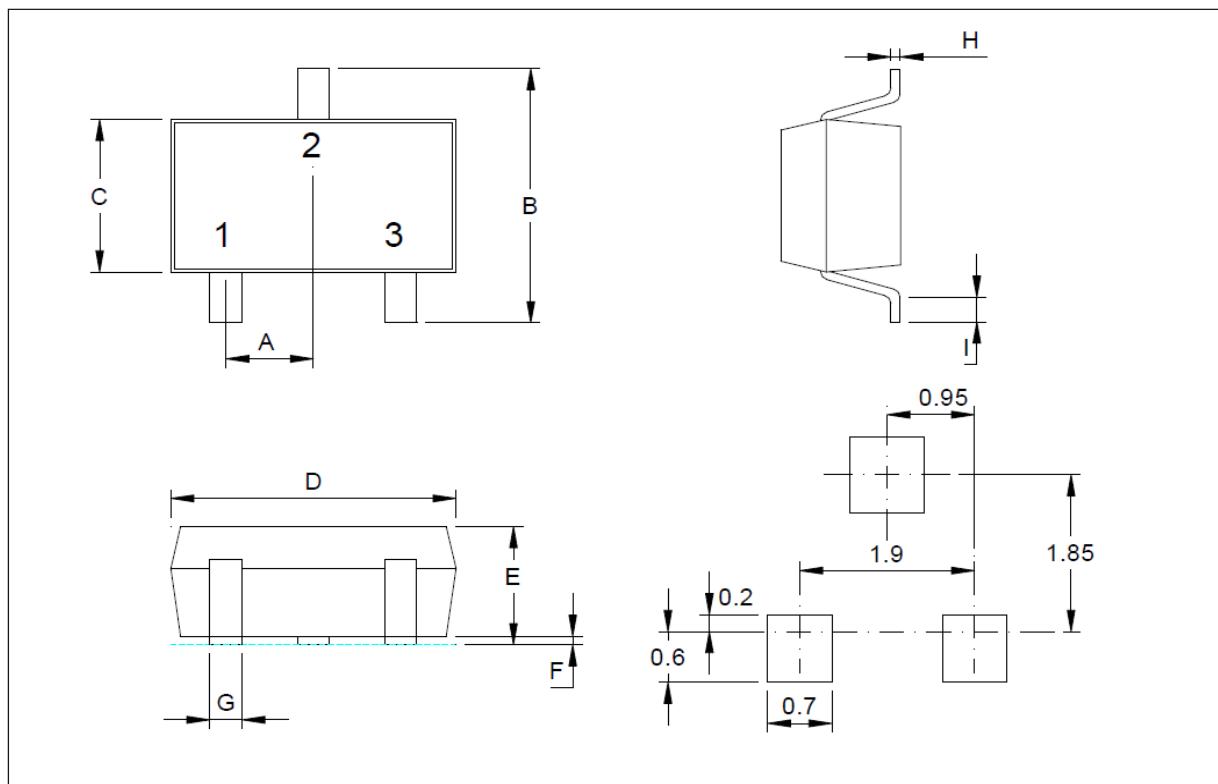
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Package Dimension

SOT-23 (S) MECHANICAL DATA

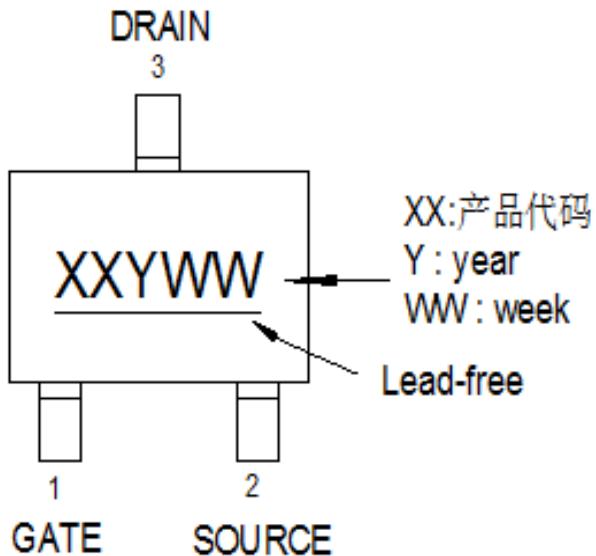
Dimension	mm			Dimension	mm		
	Min.	Typ.	Max.		Min.	Typ.	Max.
A	0.9		1	H	0.08		0.2
B	2.25		2.85	I	0.15		0.6
C	1.2		1.4				
D	2.8		3.04				
E	0.89		1.2				
F	0		0.1				
G	0.3		0.5				



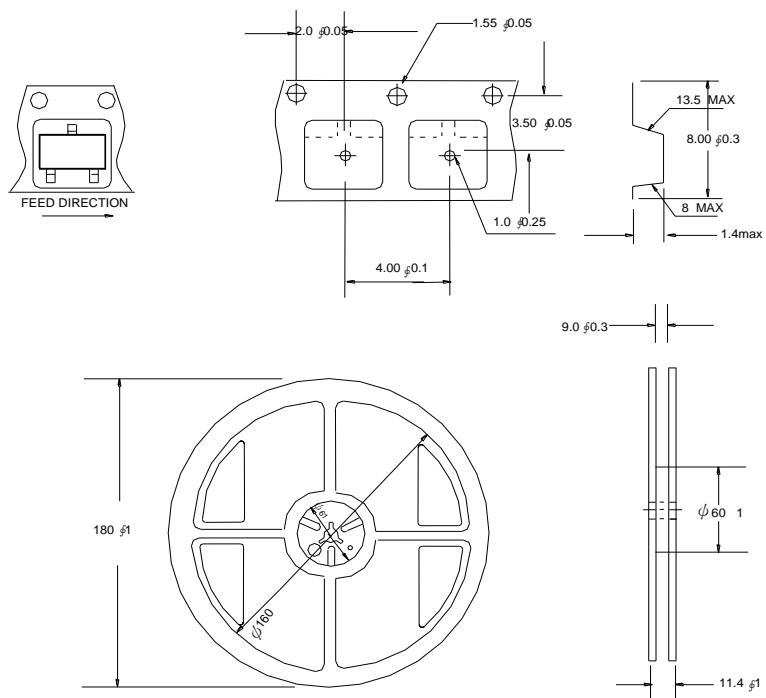
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A. Marking Information (此产品代码为：77)



B. Tape&Reel Information: 3000pcs/Reel



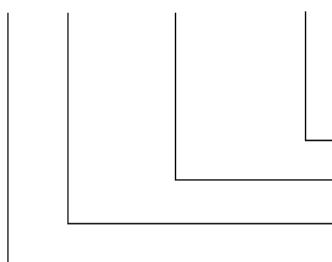
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C. Lot.No. & Date Code rule

1.LOT.NO.

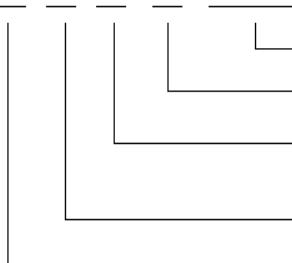
M N 15M21 03



- #8~9 Sub-lot No
- Order series no.
- Foundry site
- Assembly site

2.Date Code

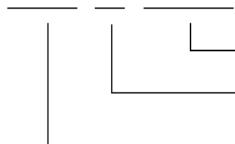
D Y M X XXX



- Order series no. & Sub-lot No
- Week
- M : Month (A:Jan , B:Feb , C:Mar ,D :Apr ,E:May ,F:Jun,G:Jul,H:Aug,I:Sep,J:Oct,K:Nov,L:Dec.)
- Y : Year (N : 2011, O : 2012 ...)
- Assembly site

3.Date Code (for Small package)

XX Y WW



- Week
- Y : Year (9: 2009,A : 2010, B : 2011 ...)
- Device Name

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D.Label rule

标签内容(Label content)



1	Label Size	30 * 90 mm
2	Font style	Times New Roman or Arial (或可区分英文“0”和数字“0”，“G”和“Q”的字型即可)
3	Great Power	Height: 4 mm
4	Package	Height: 2 mm
5	Date	Height: 2 mm Shipping date: YYYY/MM/DD, ex. 2008/09/12
6	Device	Height: 3 mm (Max: 16 Digit)
7	Lot	Height: 3 mm (Max: 9 Digit) Sub lot
8	D/C	Height: 3 mm (Max: 7 Digit)
9	QTY	Height: 3 mm (Max: 6 Digit) Thousand mark is no needed
10	Pb Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
11	Halogen Free label	 Diameter: 1 cm bottom color: Green Font color: Black Font style: Arial
12	Scan info	Device / Lot / D/C / QTY , Insert “ / “ between every parts. for example: P3055LDG/G12345601/GGG2301/2000 DPI (Dots per inch): Over 300 dpi Code : Code 128 Height: 6 mm at least