

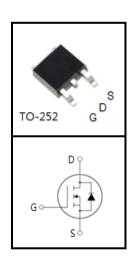
650V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information			
Device	Package	Marking	
CS4N65D	TO-252	CS4N65D	

Absolute Maximum Ratings T _C = 25°C, unless otherwise noted			
Parameter	Symbol	Value	Unit
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	650	V
Continuous Drain Current	I _D	4	Α
Pulsed Drain Current (note1)	I _{DM}	16	А
Gate-Source Voltage	V_{GSS}	±30	V
Single Pulse Avalanche Energy (note2)	E _{AS}	80	mJ
Avalanche Current (note1)	I _{AS}	4	Α
Repetitive Avalanche Energy (note1)	E _{AR}	0.32	mJ
Power Dissipation (T _C = 25°C)	P_{D}	118	W
Operating Junction and Storage Temperature Range	T_J,T_stg	-55~+150	°C

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	1.06	0000	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	80.2	°C/W	



Specifications $T_J = 25^{\circ}$ C, unless otherwise noted						
	rameter Symbol Test Conditions	-	Value			
Parameter		Min.	Тур.	Max.	Unit	
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	650			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$			±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	V
Drain-Source On-Resistance (Note3)	R _{DS(on)}	$V_{GS} = 10V, I_{D} = 2A$		2	2.4	Ω
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0V,		545		pF
Output Capacitance	C _{oss}	$V_{DS} = 25V$,		53		
Reverse Transfer Capacitance	C_{rss}	f = 1.0MHz		4.5		
Total Gate Charge	Q_g			15		nC
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V, I_{D} = 4A,$ $V_{GS} = 10V$		3		
Gate-Drain Charge	Q_gd			7		
Turn-on Delay Time	t _{d(on)}			36		
Turn-on Rise Time	t _r	$V_{DD} = 325V, I_{D} = 4A,$		13		ns
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		80		
Turn-off Fall Time	t _f			24		
Drain-Source Body Diode Character	istics					
Continuous Body Diode Current	I _S	T 0500			4	Λ
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			16	А
Body Diode Voltage	V _{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 2.0A$, $V_{GS} = 0V$			1.4	V
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_S = 4A,$		550		ns
Reverse Recovery Charge	Q_{rr}	di _F /dt =100A /μs		1.38		μC

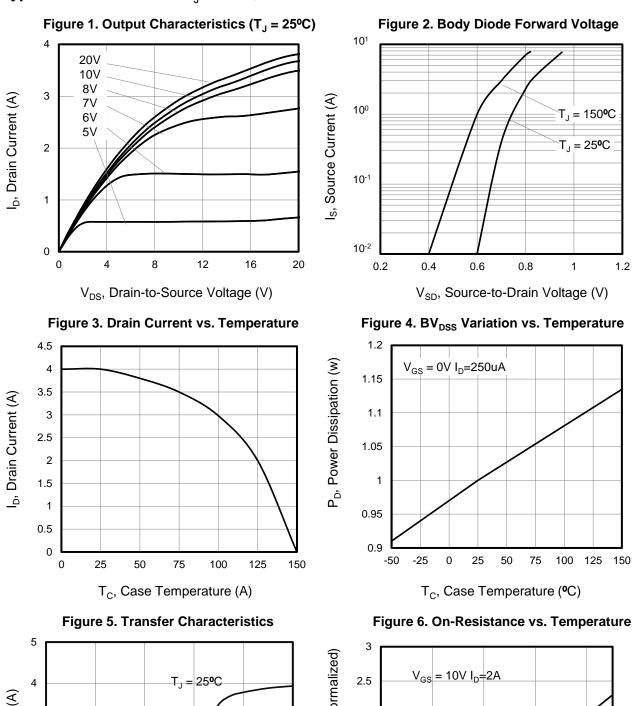
Notes

- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

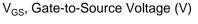
1.2

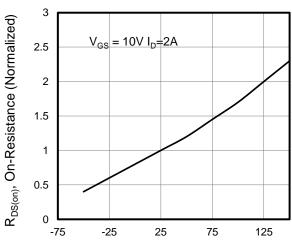


Typical Characteristics $T_J = 25^{\circ}\text{C}$, unless otherwise noted



I_D, Drain Current (A) 3 $T_J \neq 150$ °C 2 0





T_J, Junction Temperature (°C)



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

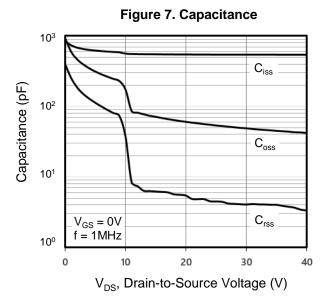


Figure 8. Gate Charge 10 V_{GS}, Gate-to-Source Voltage (V) $V_{DD} = 520 V$ 8 $V_{DD} = 325V$ $V_{DD} = 130V$ 6 4 2 0 0 2 6 8 10 12 14 16 18 Q_g, Total Gate Charge (nC)

Figure 9. Transient Thermal Impedance

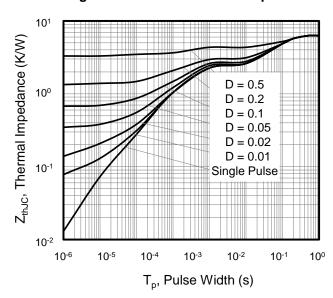




Figure A: Gate Charge Test Circuit and Waveform

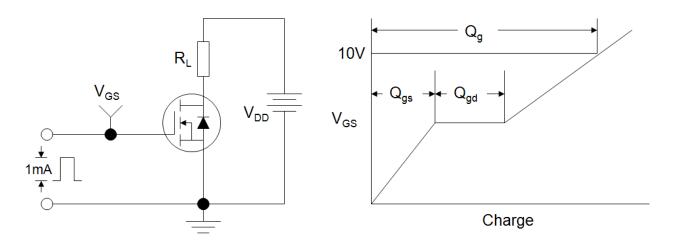


Figure B: Resistive Switching Test Circuit and Waveform

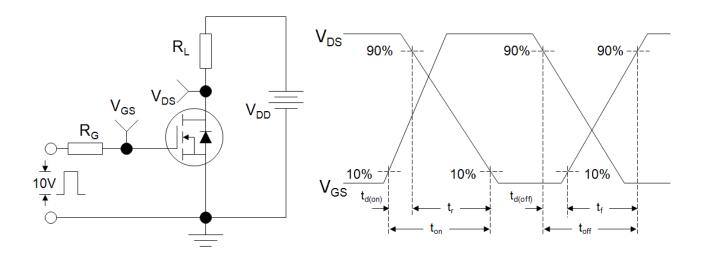
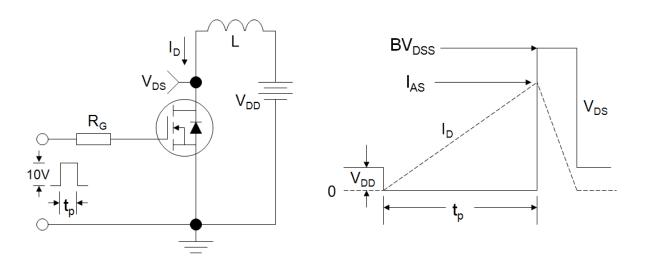
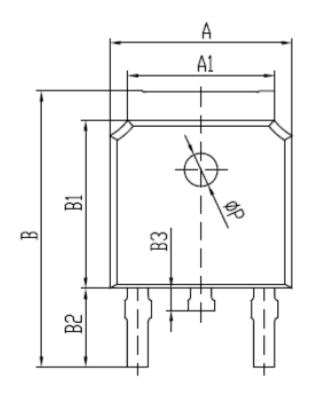


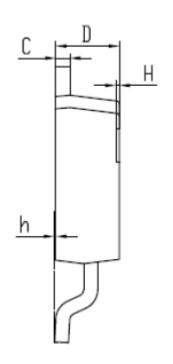
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

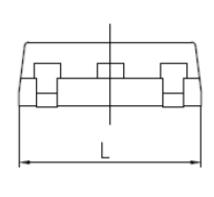


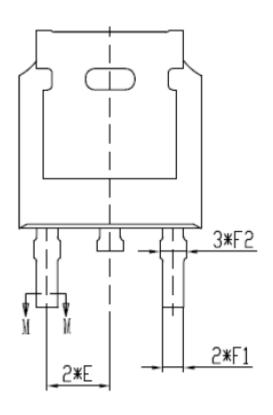


TO-252









SYMBOLS	MILLIMETERS		
STIVIDULS	MIN	MAX	
Α	6.50	6.70	
A1	5.16	5.46	
В	9.77	10.17	
B1	6.00	6.20	
B2	2.60	3.00	
В3	0.70	0.90	
С	0.45	0.61	
D	2.20	2.40	
E	2.19	2.39	
F1	0.67	0.87	
F2	0.76	0.96	
H	0.00	0.30	
h	0.00	0.13	
L	6.50	6.70	
ФР	1.10 1.30		



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