

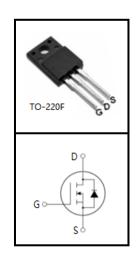
650V N-Channel MOSFET

FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability

APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



Device Marking and Package Information			
Device	Package	Marking	
CS4N65F-B	TO-220F	CS4N65F-B	

Absolute Maximum Ratings $T_C = 25^{\circ}C$, unless otherwise noted				
Parameter	Symbol	Value	Unit	
Drain-Source Voltage (V _{GS} = 0V)	V _{DSS}	650	V	
Continuous Drain Current	I _D	4	Α	
Pulsed Drain Current (note1)	I _{DM}	16	А	
Gate-Source Voltage	V_{GSS}	±30	V	
Single Pulse Avalanche Energy (note2)	E _{AS}	80	mJ	
Avalanche Current (note1)	I _{AS}	4	Α	
Repetitive Avalanche Energy (note1)	E _{AR}	0.32	mJ	
Power Dissipation (T _C = 25°C)	P_{D}	62.5	W	
Operating Junction and Storage Temperature Range	T_J,T_stg	-55~+150	°C	

Thermal Resistance				
Parameter	Symbol	Value	Unit	
Thermal Resistance, Junction-to-Case	R _{thJC}	2	00.004	
Thermal Resistance, Junction-to-Ambient	R _{thJA}	43.8	°C/W	



Specifications $T_J = 25^{\circ}C$, unless otherwise noted						
Parameter	Symbol	Tank Oan distance	Value			l lmit
		Test Conditions	Min.	Тур.	Max.	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_{D} = 250\mu A$	650			V
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = 650V, V_{GS} = 0V, T_{J} = 25^{\circ}C$			1	μΑ
Gate-Source Leakage	I _{GSS}	$V_{GS} = \pm 30V$	-		±100	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_{D} = 250 \mu A$	3.0		4.0	٧
Drain-Source On-Resistance (Note3)	R _{DS(on)}	V _{GS} = 10V, I _D = 2A		2	2.4	Ω
Dynamic						
Input Capacitance	C _{iss}	V - 0V		545		pF
Output Capacitance	C _{oss}	$V_{GS} = 0V,$ $V_{DS} = 25V,$		53		
Reverse Transfer Capacitance	C _{rss}	f = 1.0MHz		4.5		
Total Gate Charge	Q_g			15		nC
Gate-Source Charge	Q_{gs}	$V_{DD} = 520V, I_{D} = 4A,$ $V_{GS} = 10V$		3		
Gate-Drain Charge	Q_{gd}	65		7		
Turn-on Delay Time	t _{d(on)}			36		
Turn-on Rise Time	t _r	$V_{DD} = 325V, I_{D} = 4A,$		13		ns
Turn-off Delay Time	t _{d(off)}	$R_G = 25 \Omega$		80		
Turn-off Fall Time	t _f			24		
Drain-Source Body Diode Character	istics					
Continuous Body Diode Current	Is	_			4	
Pulsed Diode Forward Current	I _{SM}	T _C = 25 °C			16	A
Body Diode Voltage	V_{SD}	$T_J = 25^{\circ}C$, $I_{SD} = 2.0A$, $V_{GS} = 0V$			1.4	V
Reverse Recovery Time	t _{rr}	$V_{GS} = 0V, I_S = 4A,$		550		ns
Reverse Recovery Charge	Q _{rr}	di _F /dt =100A /μs		1.38		μC

Notes

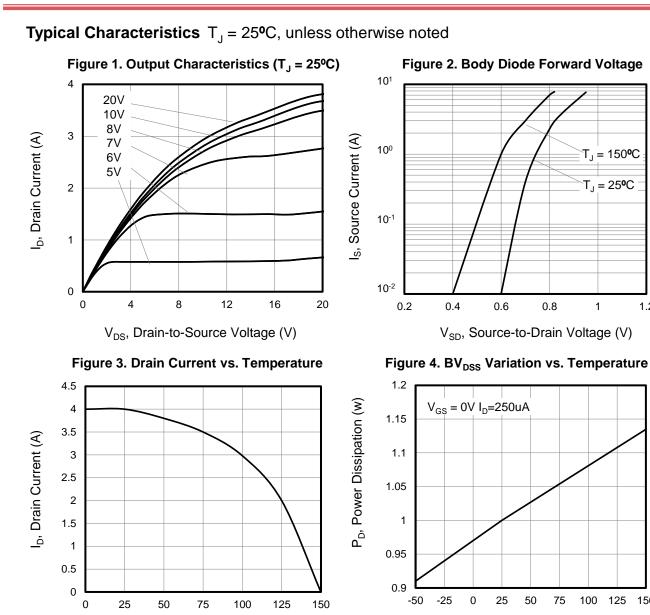
- 1. Repetitive Rating: Pulse width limited by maximum junction temperature
- 2. L = 10.0mH, V_{DD} = 50V, R_G = 25 Ω , Starting T_J = 25 $^{\circ}C$
- 3. Pulse Test: Pulse width ≤ 300µs, Duty Cycle ≤ 1%

T₁ = 150°C

 $T_1 = 25^{\circ}C$

1.2







T_C, Case Temperature (A)

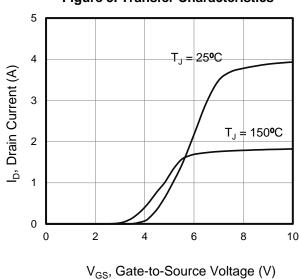
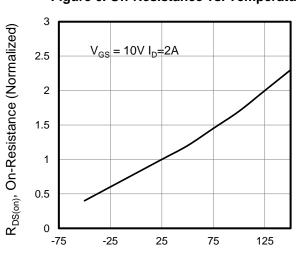


Figure 6. On-Resistance vs. Temperature

T_C, Case Temperature (°C)

75

100 125



T_J, Junction Temperature (°C)



Typical Characteristics $T_J = 25^{\circ}C$, unless otherwise noted

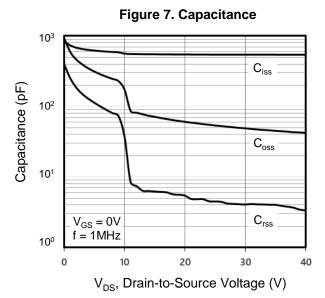


Figure 8. Gate Charge V_{GS}, Gate-to-Source Voltage (V) $V_{DD} = 325V$ $V_{DD} = 130V$ Q_g, Total Gate Charge (nC)

Figure 9. Transient Thermal Impedance

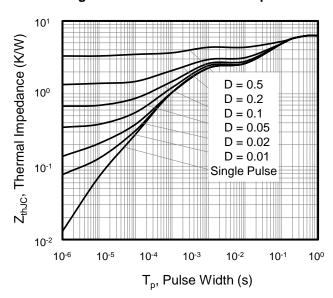




Figure A: Gate Charge Test Circuit and Waveform

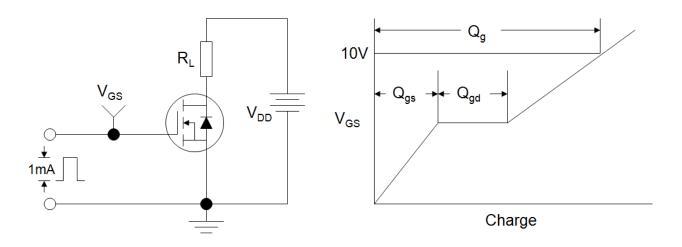


Figure B: Resistive Switching Test Circuit and Waveform

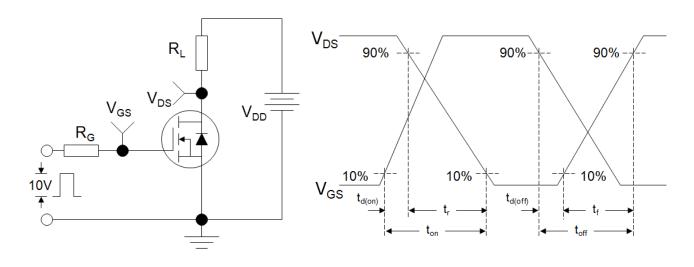
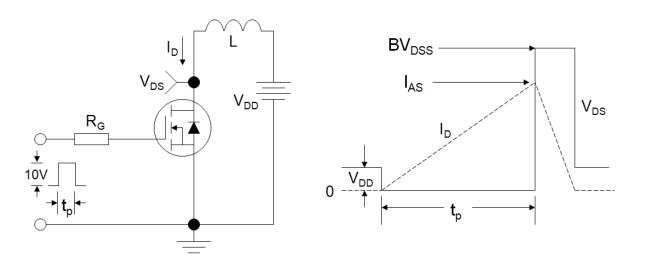
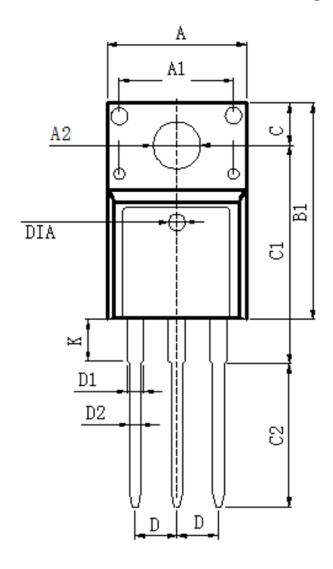


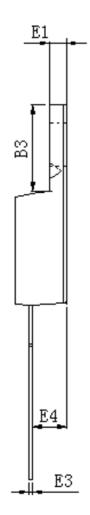
Figure C: Unclamped Inductive Switching Test Circuit and Waveform

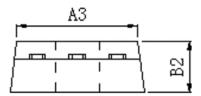




TO-220F







CANADOLC	MILLIMETERS			
SYMBOLS	MIN	MAX		
Α	9.96	10.36		
A1	6.90	7.10		
A2	3.10	3.50		
A3	9.30	9.70		
С	3.10	3.50		
B1	15.57	16.17		
B2	4.60	4.80		
В3	6.28	7.08		
C1	12.27	12.87		
C2	9.52	10.52		
D1	1.20	1.35		
D2	0.70	0.90		
K	2.80	3.40		
E1	2.44	2.64		
E3	0.40	0.60		
E4	2.46	3.06		
D	2.49	2.59		



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